

Designation	Marking	ROM / DS	Description	Notes
1LG9-0001	1LG901	ROM	12K ROM. Pages #0, #1 & #2 for C/CV	Version 0:H, 1:F, 2:F
1LG9-0008	1LG908	ROM	12K ROM. Pages #0, #1 & #2 for CX	Version 0:N, 1:F, 2:L
1LG9-0011	1LG911	ROM	12K ROM. Pages #3 and #5 banks 1 & 2 for CX	Version 3:D, 5-1:C, 5-2:C
1LE9-0006	1LE906	ROM	ROM page 0 for late C/CV	Version: H
1LE9-0007	1LE907	ROM	ROM page 1 for late C/CV	Version: F
1LE9-0008	1LE908	ROM	ROM page 2 for late C/CV	Version: F
1LB7-0038	1LB738	ROM	ROM page 0 for C/CV	Version: G
1LB7-0039	1LB739	ROM	ROM page 1 for C/CV	Version: F
1LB7-0040	1LB740	ROM	ROM page 2 for C/CV	Version: F
1LB7-0001	1LB701	ROM	ROM page 0 for C	Version: F
1LB7-0002	1LB702	ROM	ROM page 1 for C	Version: E
1LB7-0003	1LB703	ROM	ROM page 2 for C	Version: E
1LA3-0033	1LA333	ROM	ROM page 0 for C	Version: F (bug #1 correction)
1LA3-0015	1LA315	ROM	ROM page 0 for early C	Version: D
1LA3-0016	1LA316	ROM	ROM page 1 for early C	Version: D
1LA3-0022	1LA322	ROM	ROM page 2 for early C	Version: E
1LA7-0001	1LA701	DS	Base internal 16 regs. (T,Z,Y,X, etc.)	
1LA7-0002	1LA702	DS	16 "user RAM" regs. 0 – 15	16 D/S regs. HW addressed to user memory regs. 0 to 15
1LA7-0003	1LA703	DS	16 "user RAM" regs. 16 – 31	16 D/S regs. HW addressed to user memory regs. 16 to 31
1LA7-0004	1LA704	DS	16 "user RAM" regs. 32 – 47	16 D/S regs. HW addressed to user memory regs. 32 to 47
1LA7-0005	1LA705	DS	16 "user RAM" regs. 48 – 63	16 D/S regs. HW addressed to user memory regs. 48 to 63
1LE7-0001	1LE701	DS	64 "user RAM" regs. 0 – 63	64 D/S regs. HW addressed to user memory regs. 0 to 63
1LE7-0002	1LE702	DS	64 "user RAM" regs. 64 – 127	64 D/S regs. HW addressed to user memory regs. 64 to 127
1LE7-0003	1LE703	DS	64 "user RAM" regs. 128 – 191	64 D/S regs. HW addressed to user memory regs. 128 to 191
1LE7-0004	1LE704	DS	64 "user RAM" regs. 192 – 255	64 D/S regs. HW addressed to user memory regs. 192 to 255
1LE7-0005	1LE705	DS	64 "user RAM" regs. 256 – 319	64 D/S regs. HW addressed to user memory regs. 256 to 319
1LH7-0001	1LH701	DS	64 "user RAM" regs. 0 – 63	64 D/S regs. HW addressed to user memory regs. 0 to 63
1LH7-0002	1LH702	DS	64 "user RAM" regs. 64 – 127	64 D/S regs. HW addressed to user memory regs. 64 to 127
1LH7-0003	1LH703	DS	64 "user RAM" regs. 128 – 191	64 D/S regs. HW addressed to user memory regs. 128 to 191

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1LH7-0004	1LH704	DS	64 "user RAM" regs. 192 – 255	64 D/S regs. HW addressed to user memory regs. 192 to 255
1LH7-0005	1LH705	DS	64 "user RAM" regs. 256 – 319	64 D/S regs. HW addressed to user memory regs. 256 to 319
1LH7-0006	1LH706	DS	64 "Extended Memory" regs. 0 – 63	62 D/S regs. HW addressed to (CX) X-MEM regs. 1 to 62
1LH7-0007	1LH707	DS	64 "Extended Memory" regs. 64 – 127	62 D/S regs. HW addressed to (CX) X-MEM regs. 65 to 126